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TRANSMITTAL FORM

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		Application Number	10/719,998
		Filing Date	November 21, 2003
		First Named Inventor	Takashi Fujise
		Art Unit	2838
		Examiner Name	Jeffrey L. Sterrett
Total Number of Pages in This Submission		Attorney Docket Number	9319S-000579

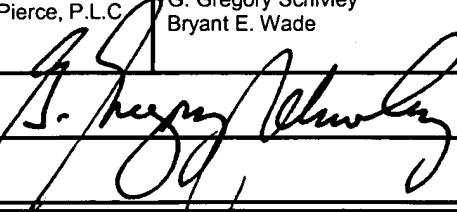
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<input type="checkbox"/> After Final	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Proprietary Information
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<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		

Remarks

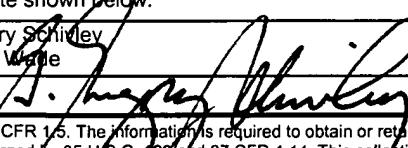
The Commissioner is hereby authorized to charge any additional fees that may be required under 37 CFR 1.16 or 1.17 to Deposit Account No. 50-3213. A duplicate copy of this sheet is enclosed.

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Harness, Dickey & Pierce, P.L.C.	Attorney Name G. Gregory Schivley Bryant E. Wade	Reg. No. 27,382 40,344
Signature			
Date	March 22, 2006		

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/719,998

Filing Date: November 21, 2003

Applicant: Takashi Fujise

Group Art Unit: 2838

Examiner: Jeffrey L. Sterrett

Title: STEP-DOWN CIRCUIT, POWER SUPPLY CIRCUIT, AND SEMICONDUCTOR INTEGRATED CIRCUIT

Attorney Docket: 9319S-000579

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT

Sir:

In response to the Office Action mailed December 23, 2005, please amend the application as follows and consider the remarks set forth below.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 5 of this paper.